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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/777,142	02/13/2004	Shigeo Ide	107156-00229	3787

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EXAMINER	
AL NAZER, LEITH A	

ART UNIT	PAPER NUMBER
2821	

DATE MAILED: 03/15/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/777,142	IDE ET AL.	
	Examiner	Art Unit	
	Leith A. Al-Nazer	2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *22 November 2005*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-8* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-8* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *13 February 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent Application Publication No. 2001/0033355 to Hagiwara.

With respect to claims 1 and 2, Hagiwara teaches a flat panel display apparatus, comprising: a display section main body (300) having a plurality of display electrodes (15 and 25) arranged thereon; and a tape carrier package (paragraphs 0053 and 0054) connected to the display electrodes, wherein one end of the display section main body is formed with: i) a first terminal group (group between reference number 94 in figure 1) connected to the plurality of display electrodes, ii) a second terminal group (94) positioned adjacent to the first terminal group, wherein the second terminal group comprises a first plurality of connecting terminals (figures 1 and 2), and iii) a third terminal group (80) comprising a second plurality of connecting terminals (figures 1 and 2), wherein each of the first plurality of connecting terminals is directly connected to a corresponding one of the second plurality of connecting terminals via at least one wire (figures 1 and 2), wherein a connection end of the tape carrier package (70; paragraphs 0053 and 0054) is connected to the first terminal group and the second terminal group,

and wherein a flat cable (8) for external wiring is connected with the third terminal group.

With respect to claim 3, Hagiwara teaches the first and second terminal groups being collected in a predetermined area at one end of the display section main body (figure 1), the connection end of the tape carrier package (70; paragraphs 0053 and 0054) being connected through one operation to the first and second terminal groups formed and collected in the predetermined area.

With respect to claim 4, Hagiwara teaches the first and second terminal groups being formed by being arranged in one column (figure 1) in the predetermined area at one end of the display section main body.

With respect to claim 5, Hagiwara teaches the tape carrier package being formed having connecting terminal groups to be connected to the first and second terminal groups located in a connecting terminal section, and an electronic device (7) being connected between the connecting terminal group to be connected to the first terminal group and the connecting terminal group to be connected to the second terminal group.

With respect to claim 6, Hagiwara teaches the electronic device being an integrated circuit device (7) for supplying an electric power for display to the plurality of display electrodes.

With respect to claim 7, Hagiwara teaches the flat cable being connected between a circuit board and the third terminal group (figure 3).

With respect to claim 8, Hagiwara teaches an interval between the first, second terminal groups and the third terminal group being larger than a length of the tape carrier package (figure 3).

Response to Arguments

3. Applicant's arguments filed 22 November 2005 have been fully considered but they are not persuasive.

Applicant argues that in the system of Hagiwara, only two of the connecting terminals of the second terminal group 94 are directly connected to a corresponding one of the connecting terminals of the third terminal group 80. Applicant further argues that a single wire is used to connect all of the connecting terminals of the second terminal group 94 to all of the connecting terminals of the third terminal group 80, such that all but the outer most end connection terminals of the second terminal group 94 are indirectly connected to the third terminal group 80. Examiner disagrees. Figures 1 and 2 clearly show a plurality of wires leading from the third terminal group to the first and second terminal groups via element 70, which can be a tape carrier package, as is outlined in paragraphs 0053 and 0054. As a result, Examiner believes that Hagiwara teaches all of the limitations recited in amended independent claim 1, as well as dependent claims 2-8, as is outlined in the rejection under 35 USC 102(b) above.

Communication Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leith A. Al-Nazer whose telephone number is 571-272-1938. The examiner can normally be reached on Monday-Friday, 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on 571-272-1834. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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TRINH DINH
PRIMARY EXAMINER

